1. MECHANICAL DATA

(1) Product No.	AGM6448S
(2) Module Size	264.0 (W)mm x 183.0 (H)mm x MAX10.0 (D)mm
(3) Dot Size	0.305 (W)mm x 0.305 (H)mm
(4) Dot Pitch	0.33 (W)mm x 0.33 (H)mm
(5) Number of Dots	640 (W) x 480 (H)Dots
(6) Duty	1/240
(7) LCD Display Mode FSTN:	Black and White(Normal White/Positive Image)
Rear Polarizer:	Transflective
(8) Viewing Direction	6 O'clock
(9) Backlight	CCFL
(10) Controller	Excluded
(11) DC/DC Converter	Excluded
(12) Weight	450.0 g(approx.)
(13) Recommended FL Inverter	TAD250(TDK)

Revised: October 23, 2001

2. ABSOLUTE MAXIMUM RATINGS

(1) ELECTRICAL ABSOLUTE RATINGS

VSS=0 V Standard

ITEM	SYMBOL	MIN	MAX	UNIT	COMMENT
Power Supply for Logic	VDD-VSS	-0.3	6.5	V	
Power Supply for LCM	VEE-VSS	0	27.0	V	
Input Voltage	VI	-0.3	VDD+0.3	V	
Static Electricity	_	_	_	_	Note 1

Note 1 LCM should be grounded during handling.

(2) ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

	NORMAL TEMP.						
ITEM	OPER	ATION	STORAGE				
	MIN.	MAX.	MIN.	MAX.			
Ambient Temperature	0	50	-20	70			
Humidity (Without Condensation)	Note	2,4	Note	e 3,4			
Vibration(Note *)	-	_	49m/	s² (5G)			

Note 2 Ta ≤ 50°C : 85%RH max

Ta > 50°C : Absolute humidity must be lower

than the humidity of 85%RH at 50°C

Note 3 Ta at -20° C will be < 48 hrs, at 70° C will be < 120 hrs

Note 4 Background color will change slightly depending on ambient temperature.

This phenomenon is reversible.

Note**፠**

Frequency (HZ)	10~55~10/1 min
Vibration Width	1.5 m/m
Vibration Direction	X/Y/Z
Vibration Time	15 min/cycle X 3 directions

3. ELECTRICAL CHARACTERISTICS

	ITEM	SYMBOL	CONDITI	ON	MIN.	TYP.	MAX.	UNIT
Power Supply		VDD-VSS				5.0	5.5	V
for Lo	gic	\DD-\23	_		2.7	3.0	3.3	V
				0°C	_	_	_	
	mended ving Voltage	VEE-VSS	Duty=1/240 Bias=1/13	25°C	_	23.4	_	V
	3		,	50°C	_	_	_	
	7. 11	VIH	H leve	el	0.8VDD	_	VDD	V
Input \	voitage	VIL	L leve	I	0	_	0.2VDD	V
		IDD	FLM = 70 Hz VDD = 5.0 V VEE-VSS = 23.4 V		-	2.0	4.0	mA
Fower	Power Supply Current		PATTERN:		_	10.0	20	mA
	Starting Voltage	Vs			_	750	_	Vrms
	Lamp Voltage	VL			_	640	_	Vrms
CCFL	Lamp Current	IL			3	5	6	mArms
LAMP	Lamp Consumption	PL			_	3.05	_	W
	Lamp Frequency	FL			_	45	_	KHz
	Lamp Life Time	LL	NOTE 1		10000	_	_	hrs
			ALL ON		_	TBD	_	cd/m²
LCM	Surface Luminance	L	ALL OFF		_	TBD	_	
	Luminance Uniformity	Lu			_	TBD	_	%

NOTE 1: The life is defined as the time it takes the brightness to reduce to 50% of its original value.

AGM6448S

4. OPTICAL CHARACTERISTICS

AT Vop

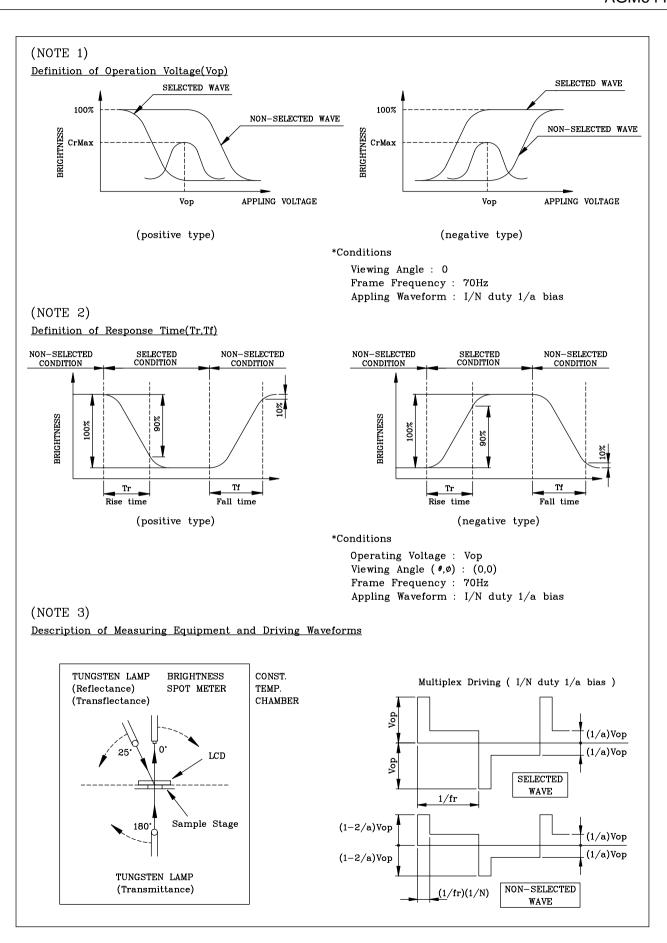
	ITEM	Cr(Contrast Ratio)		θ(Viewin	g Angle)	<pre></pre>		
		25°t		25°C		25°C		
MODE		MIN.	TYP.	MIN.	TYP.	MIN.	TYP.	
S	Р	_	TBD	_	TBD	_	TBD	
NOTE		NO ⁻	NOTE6		NOTE5			

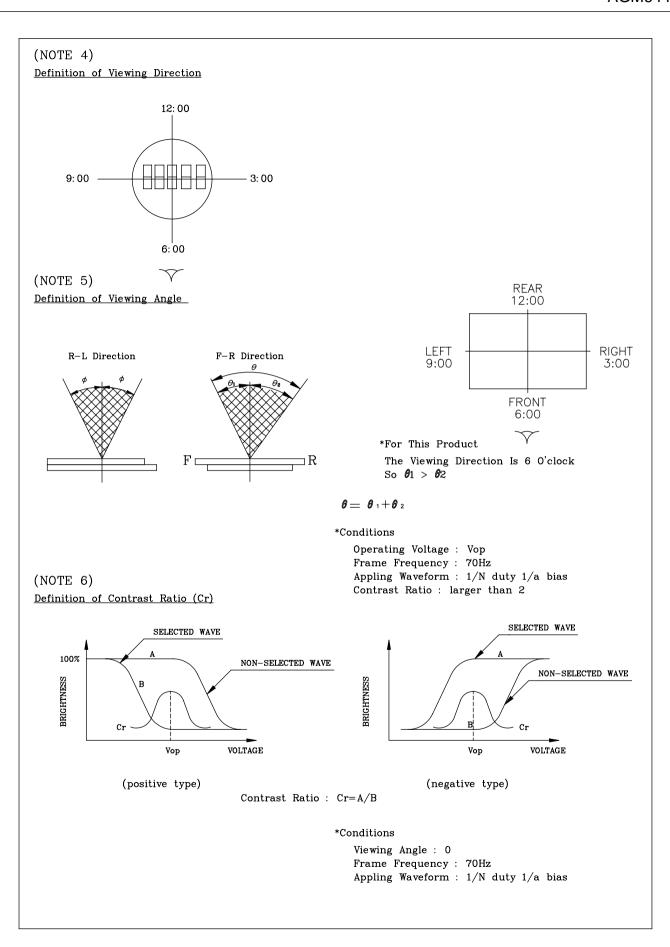
S: Transflective

P: Normally White, 6 o'clock

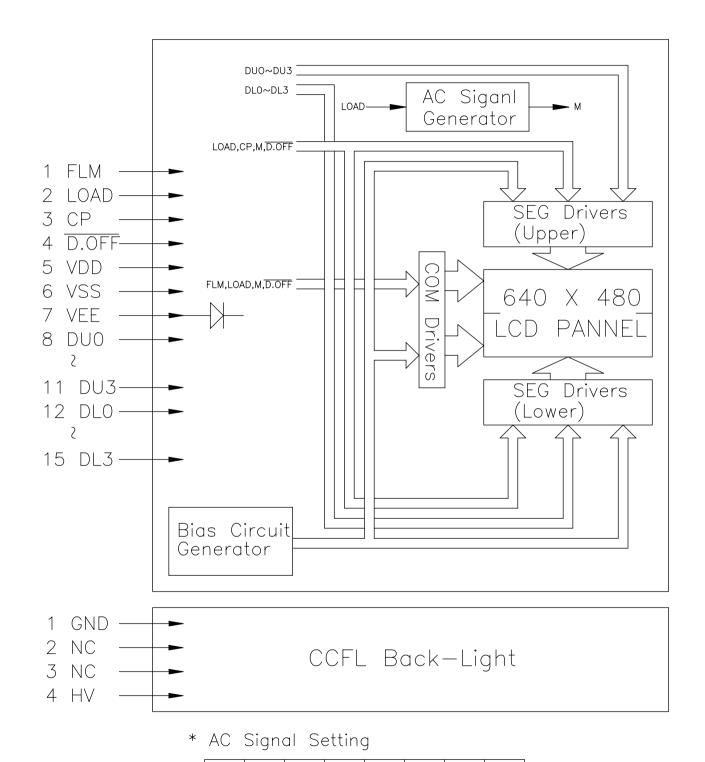
AT $\emptyset = 0^{\circ} \theta = 0^{\circ}$

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	NOTE
		0°C	_	TBD	_		
Response Time (rise)	Tr	25°C	_	TBD	_	ms	NOTE 2
		50°C	_	TBD	_		
		0°C	_	TBD	_		
Response Time (fall)	Tr	25°C	_	TBD	_	ms	NOTE 2
		50°C	_	TBD	_		





5. BLOCK DIAGRAM



J2

J1

J3

J4

Н

J5

J7

J8

J6

6. INTERNAL PIN CONNECTION

LCD

Pin No.	Symbol	Level	Function				
1	FLM	H/L	SCAN START-UP SIGNAL				
2	LOAD	H→L	DATA LATCH PULSE				
3	CP	H→L	DATA SHIFT PULSE				
4	D.OFF	H/L	DISPLAY OFF ("H"=ON,"L"=OFF)				
5	VDD	_	POWER SUPPLY FOR LOGIC (+5V)				
6	VSS	_	SIGNAL GROUND (GND)				
7	VEE	_	POWER SUPPLY FOR LCD (+V)				
8	DUO						
9	DU1	11/1	DICDLAY DATA (LIDDED LIALE)				
10	DU2	H/L 	DISPLAY DATA (UPPER HALF)				
11	DU3						
12	DLO						
13	DL1		DICDLAY DATA (LOWED LIALE)				
14	DL2	H/L	DISPLAY DATA (LOWER HALF)				
15	DL3						

CCFT

Pin No.	Symbol	Level	Function
1	GND	_	GROUND LINE (INVERTER)
2	NC	_	NON CONNECTION
3	NC	_	NON CONNECTION
4	HV	_	HIGH VOLTAGE LINE (INVERTER)

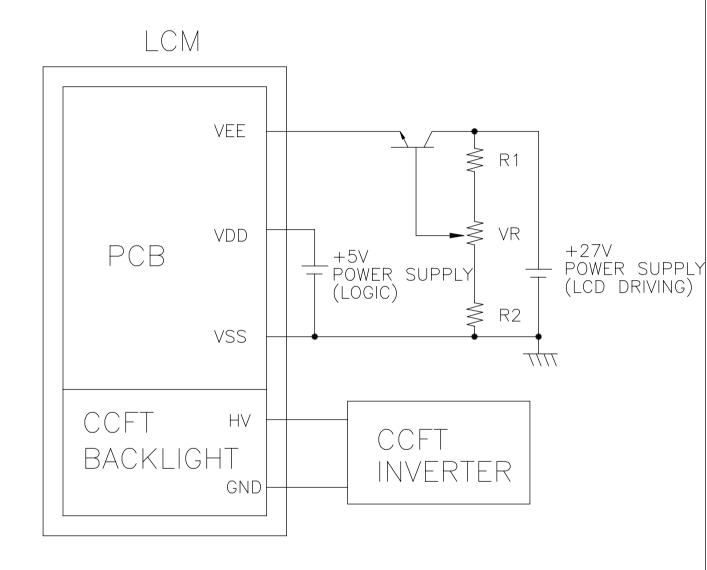
LCD

Used connector: 53261-1590 (MOLEX)

CCFT

USE CONNRCT: M63M83-04(MITSUMI)

7. POWER SUPPLY



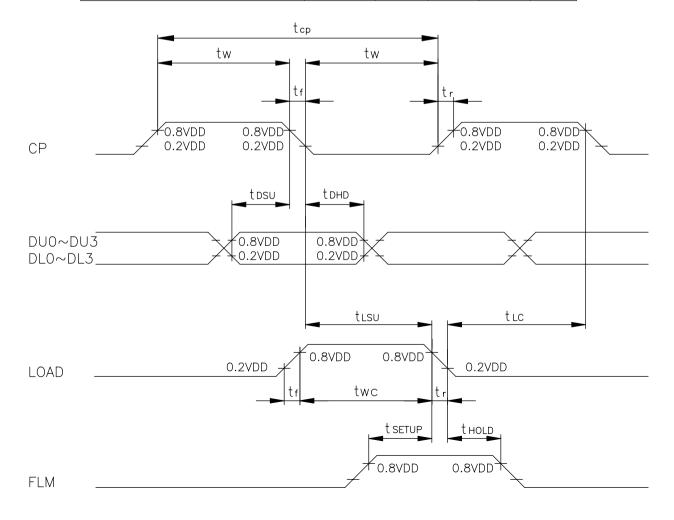
- $1.R1 + VR + R2 = 10K \sim 20K\Omega$
- 2.RECOMMENDED CCFT INVERTER: TAD250(TDK)

8. TIMING CHARACTERISTICS

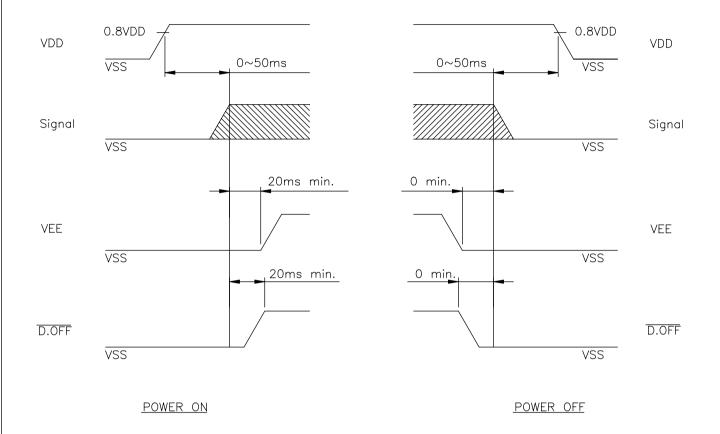
8-1.INTERFACE TIMING

 $@VDD = 2.5 \sim 5.5V$

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
Shift Clock Period	tcp	125	_	_	ns
"CP" PULSE WIDTH	tw	51	_	_	ns
CLOCK RISE, FALL TIME	tr, tr	_	_	20	ns
DATA SETUP TIME	tosu	40		_	ns
DATA HOLD TIME	t DHD	30	_	_	ns
"CP"→ "LOAD" FALL TIME	tısu	51	_	_	ns
"LOAD" → "CP" FALL TIME	tLC	51	_	_	ns
"FLM" SETUP TIME	t SETUP	30	_	_	ns
"FLM" HOLD TIME	t HOLD	50	_	_	ns
"LOAD" PULSE WIDTH	t wc	51	_	_	ns

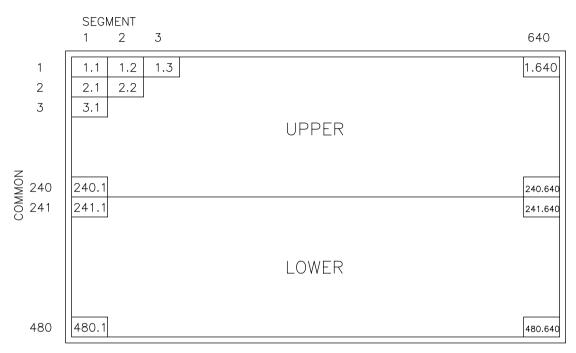


8-2. POWER ON/OFF TIMING

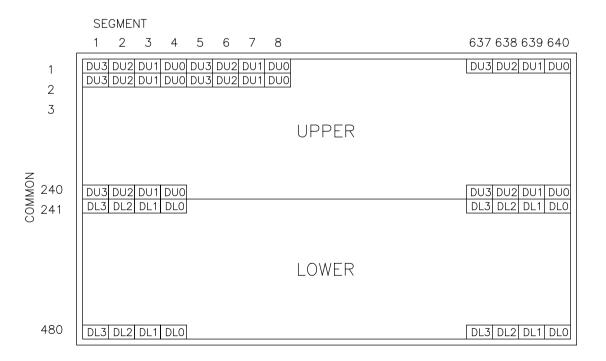


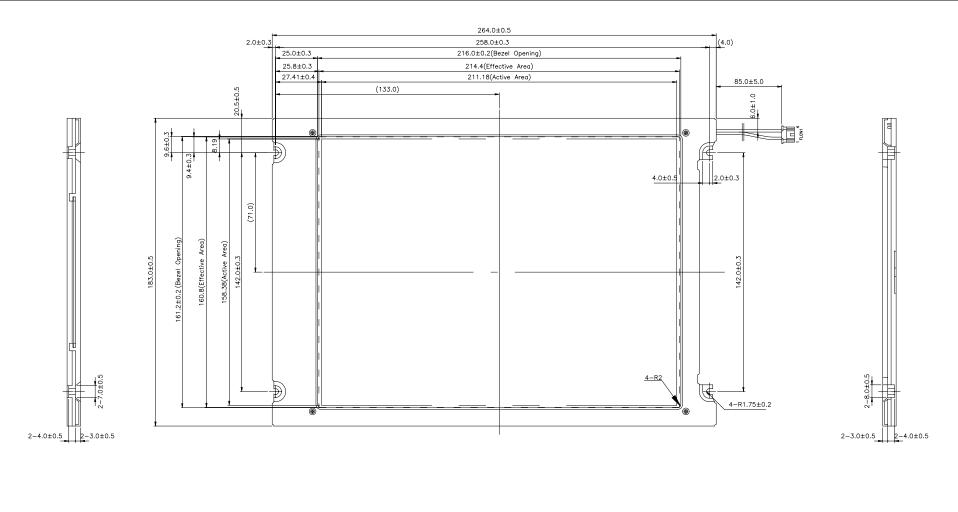
Missing pixels may occur when the LCM is driven beyond the above power interface timing sequence.

9. DISPLAY PATTERM



NOTE: 1.1 MEANS 1ST COMMON 1ST SEGMENT DOT







CN1 :MOLEX 53261-1590

PIN#	1	2	3	4	5	6	7	8
SYMBOL	FLM	LOAD	CP	D.OFF	VDD	VSS	VEE	DUO
PIN#	9	10	11	12	13	14	15	-
SYMBOL	DU1	DU2	DU3	DLO	DL1	DL2	DL3	-

FLCN1 : M63M83-04(MITSUMI)

		PIN NO.	SYMBOL	FUNCTION
		1 114 140.	GND	
	FLCN1	1		CFL GND
		2	NC	-
		3	NC	_
		4	HV	Power supply voltage for CFL

AZ DISPLAYS, INC.

AGM6448S

ΛI),														
	DIMENSION	TOLERANCE									NAME	DATE	THIRD A	NGLE P.
	L ≤ 6	±0.25	(mm)	Ś						APPROVE			Φ.	
	6 < L ≤ 18	±0.3	(mm)	4						CHECK			\$\psi\$	
	18 < L ≤ 50	±0.4	(mm)	3						DESIGN			SCALE	UNIT
	50 < L ≤ 125	± 0.5	(mm)	<u> </u>						DRAWN			NONE	mm
	125 < L	± 0.6	(mm)	\triangle						DWG NO.	M4[3[4		[7] (1	> \(\)
	ANGLE	±1°	(DEG)	REV. NO.	DESCRIPTION	DATE	DESIGN	CHECK	APPROVE	DWG NO.	1111141914	1_1010		7 (\