

AZ DISPLAYS, INC.

COMPLETE LCD SOLUTIONS

SPECIFICATIONS FOR LIQUID CRYSTAL DISPLAY

PART NUMBER:

AGM1264R Series

REVISED:

FEB 14, 2007

1. General Specifications

1. Features

- A. Drive Method: 1/64 Duty, 1/9 Bias
- B. The Module Operating Voltage: 3.3V;
- C. The LCD Operating Voltage : 9.7V;
- D. Viewing Direction: 6:00h
- E. Operating Temperature: -20 ~70
- F. Storage Temperature: -30 ~85
- G. Display mode: FSTN mode, positive type display

2. Characteristics of backlight (LED unit)

Item	Symbol	Min.	Typ.	Max.	Unit	Condition
Forward Voltage	VF	2.85	3.2	3.5	V	IF=90mA
Forward Current	IF		90	120	mA	
Luminous	LV		60		cd/m ²	IF=90mA
Color	BLUE					

WARNING:

A BACKLIGHT IS A KIND OF CURRENT DEVICE,IT MUST CONNECT A RESISTANCE FOR LIMITING CURRENT ,OR IT WILL BE DAMAGED.

3. Mechanical Data:

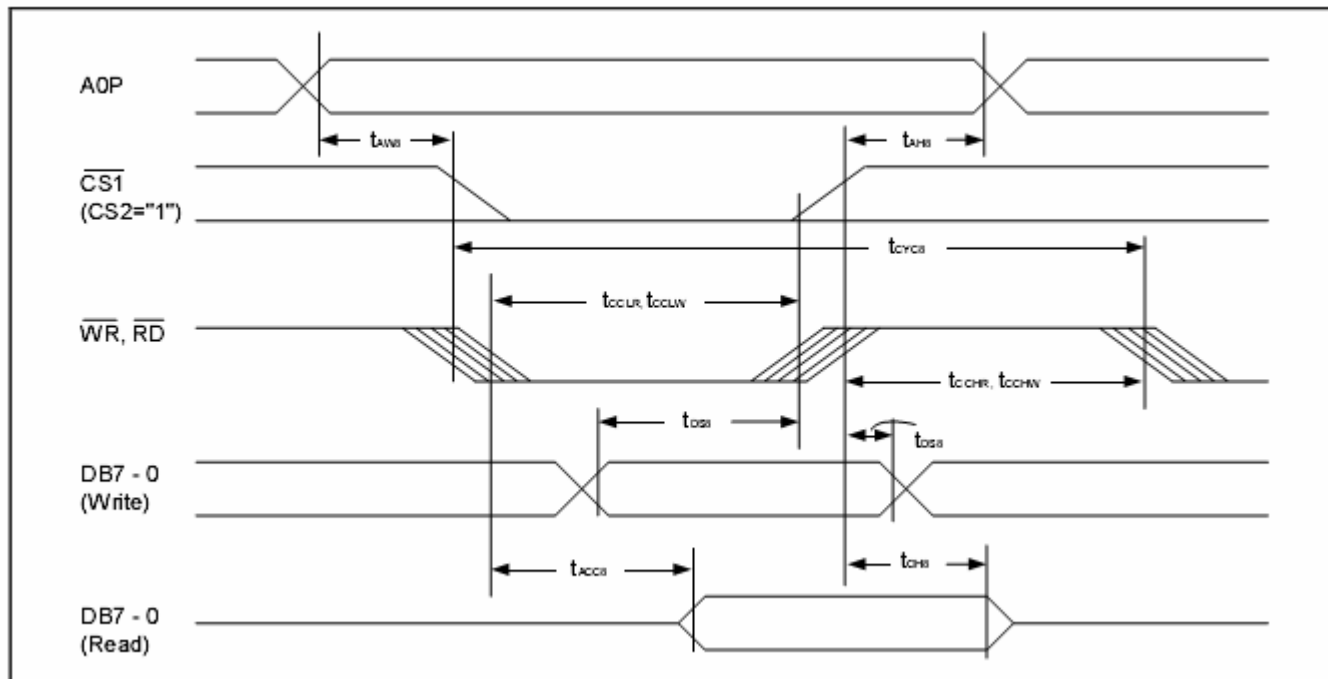
- (1) Module Size ----- 72.0 w * 90.0 h mm
- (2) Viewing Area ----- 66.4 w * 39.4 h mm
- (3) Dot Size ----- 0.45 w * 0.53 h mm
- (4) Number of Dots ----- 128 * 64 Dots
- (5) Outline Dimensions----- See Attached Drawing

4. Pin Connections:

Pin No.	Symbol	LEVEL	Function
1	P/S	INPUT	This is the parallel data input/serial data input switch terminal
2	C86	INPUT	This is the MPU interface switch terminal
3-7	V5-V1	INPUT	This is a multi-level power supply for the liquid crystal drive $VDD \geq V1 \geq V2 \geq V3 \geq V4 \geq V5$
8	CAP2+	OUTPUT	DC/DC voltage converter capacitor 2 positive connection
9	CAP2-	OUTPUT	DC/DC voltage converter capacitor 2 negative connection
10	CAP1-	OUTPUT	DC/DC voltage converter capacitor 1 negative connection
11	CAP1+	OUTPUT	DC/DC voltage converter capacitor 1 positive connection
12	CAP3-	OUTPUT	DC/DC voltage converter capacitor 3 negative connection
13	VOUT	OUTPUT	DC/DC converter output
14	VSS	0V(GND)	Groud
15	VDD	+5V	Power supply for logic
16-23	D7-D0	H/L	Data bit
24	/RD(E)	H/L	Signal to select read and write
25	/WR(R/W)	H/L	Signal to select read and write
26	A0	H/L	Control/data select signal
27	/RES	H,H-L	Reset signal
28	/CS1	H/L	Chip select signal

5. Timing Characteristics:

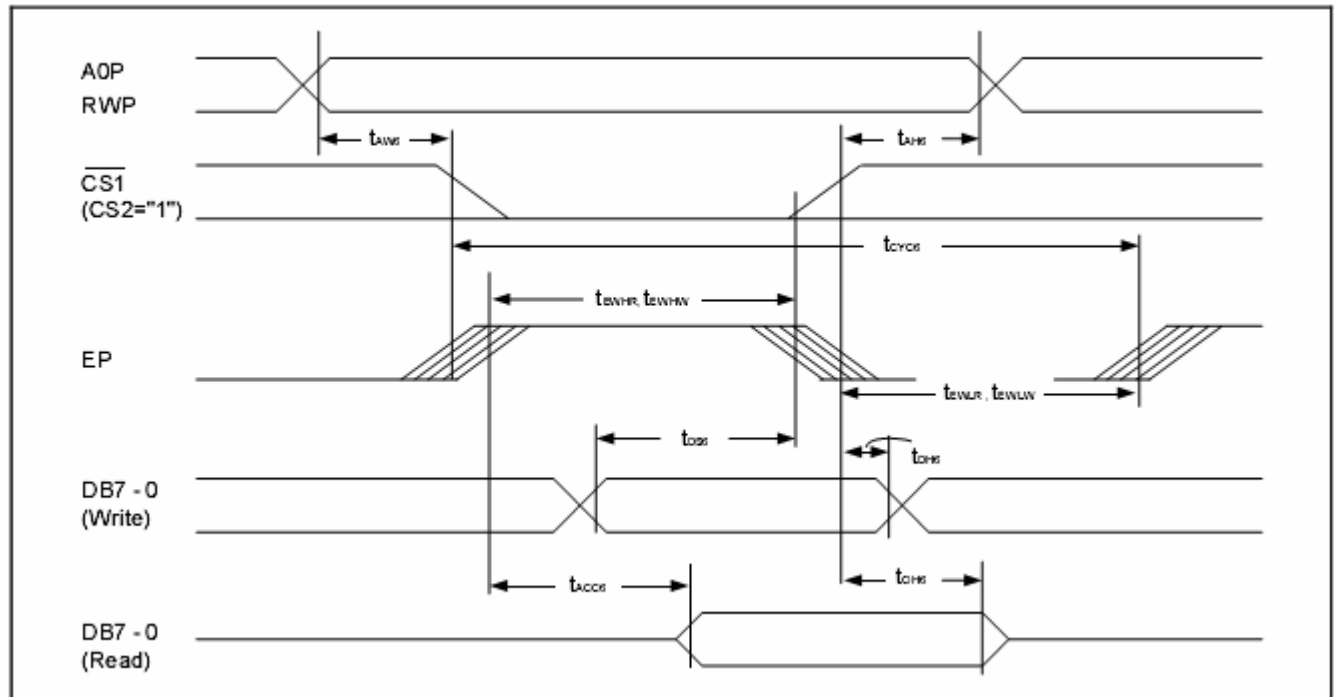
8.6.1. System bus read/write characteristics 1 (For the 8080 Series MPU)



(VDD = 2.7V to 4.5V, T_A = 25°C)

Item	Signal	Symbol	Condition	Rating		Units
				Min.	Max.	
Address hold time	A0P	t_{AHS}		0	-	ns
Address setup time	A0P	t_{AHS}		0	-	ns
System cycle time	A0P	t_{CVCS}		300	-	ns
Control L pulse width (WR)	WR	t_{CCLW}		60	-	ns
Control L pulse width (RD)	RD	t_{CCLR}		120	-	ns
Control H pulse width (WR)	WR	t_{CCHW}		60	-	ns
Control H pulse width (RD)	RD	t_{CCHR}		60	-	ns
Data setup time	DB7 - 0	t_{OSS}		40	-	ns
Address hold time		t_{OHS}		15	-	ns
RD access time		t_{ACS}	$C_L = 100pF$	-	140	ns
Output disable time		t_{OHS}		10	100	ns

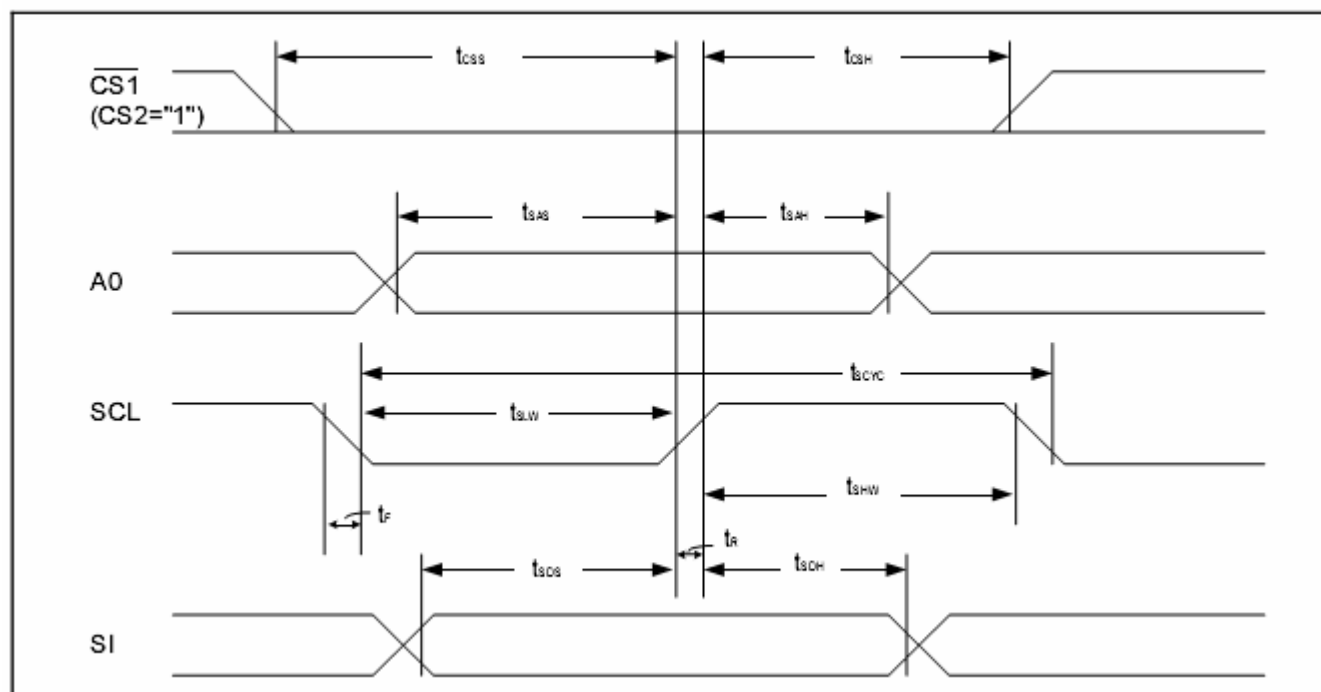
8.6.2. System bus read/write characteristics 2 (6800 series MPU)



(VDD = 2.7V to 4.5V, T_A = 25°C)

Item	Signal	Symbol	Condition	Rating		Units
				Min.	Max.	
Address hold time	A0P	t_{AHS}		0	-	ns
Address setup time	A0P	t_{AVS}		0	-	ns
System cycle time	A0P	t_{CYS}		300	-	ns
Data setup time	DB7 - 0	t_{OSS}	$C_L = 100pF$	40	-	ns
Data hold time		t_{OHS}		15	-	ns
Access time		t_{ACS}		-	140	ns
Output disable time		t_{OHS}		10	100	ns
Enable H pulse time	Read	EP	t_{EHHR}	120	-	ns
	Write		t_{EHWW}	60	-	ns
Enable L pulse time	Read	EP	t_{EWLR}	60	-	ns
	Write		t_{EWUW}	60	-	ns

8.6.3. The serial interface



(VDD = 2.7V to 4.5V, T_A = 25°C)

Item	Signal	Symbol	Condition	Rating		Units
				Min.	Max.	
Serial Clock Period	SCL	t_{CYC}	-	250	-	ns
SCL 'H' pulse width		t_{SHW}	-	100	-	ns
SCL 'L' pulse width		t_{SLW}	-	100	-	ns
Address setup time	A0P	t_{SAS}	-	150	-	ns
Address hold time		t_{SAH}	-	150	-	ns
Data setup time	SI	t_{SDS}	-	100	-	ns
Data hold time		t_{SDH}	-	100	-	ns
CS-SCL time	CS	t_{CSS}	-	150	-	ns
		t_{CSH}	-	150	-	ns

2.The Characteristics and The Reliability Test

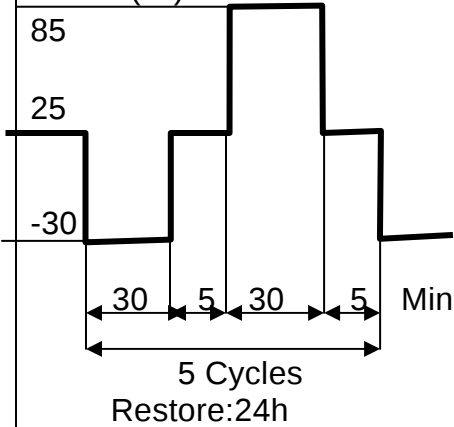
1.Electro-Optic Characteristics:

Condition:TEMP=(23 ± 3) Hum=(70 ± 5)%RH

V_{dd}: 5.0V

NO	Item		Symbol	Min	Typ.	Max	Unit	Condition
1	Supply Voltage(Logic)		Vdd-Vss	3.0	3.3	3.6	V	
2	LCD Operating Voltage		Vdd-V ₀		10.1		V	-20
					9.7		V	25
					9.3		V	70
3	Response Time		Ton		60		ms	
			Toff		316		ms	
4	Contrast		CR	3				
5	Viewing Angel	12H	θ 1		45		Deg	(CR ≥ 3.0)
		6H	θ 2		55			
		3H	θ 3		50			
		9H	θ 4		50			
6	LCD Threshold Voltage		Vth		7.78		V	25

3. Reliability Test

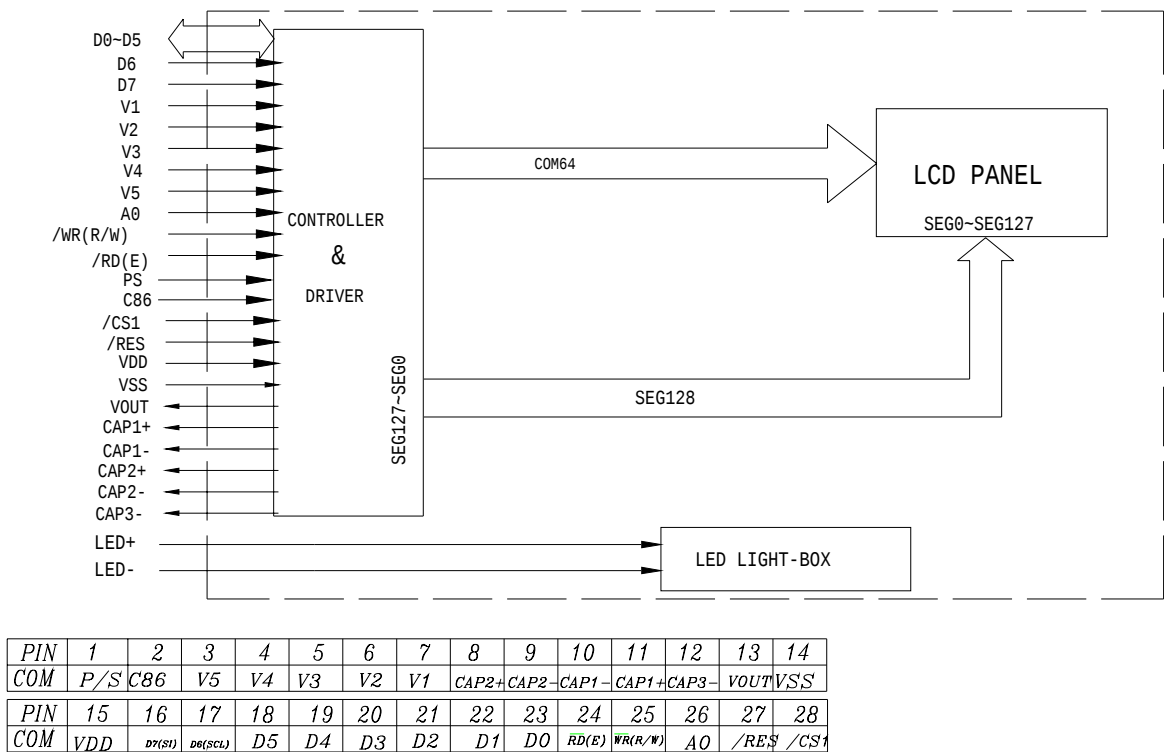
No	Items	Test Condition	Equipment	Test Result
1	High TEMP Storage	TEMP: 85 ± 2 Time: 96h Restore: 24h	Tenny	Passed
2	Low TEMP Storage	TEMP: -30 ± 3 Time: 96h Restore: 24h	Tenny	Passed
3	High TEMP Operating	TEMP: 70 ± 2 Vop: 5V Timp: 24h Restore: 24h	Tenny	Passed
4	Low TEMP Operating	TEMP: -20 ± 2 Vop: 5V Timp: 24h Restore: 24h	Tenny	Passed
5	High TEMP High Hum Storage	TEMP: 40 ± 2 Hum: 95%Rh Time: 96h Restore: 24h	Tenny	Passed
6	Thermal Shock	TEMP: ()  The diagram shows a thermal shock test profile with five cycles. The temperature levels are 85, 25, and -30. The dwell times are 30 minutes at 85, 5 minutes at 25, and 30 minutes at -30. The transition times are 5 minutes. The total test time is 5 cycles. The equipment used is Tenny and the result is Passed. 5 Cycles Restore: 24h	Tenny	Passed

4. Instruction Sets

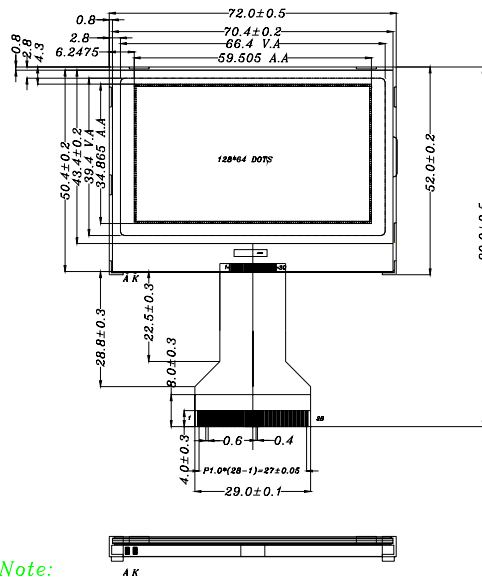
Command	Command Code											Function
	A0P	RD	WR	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	
1). Display ON/OFF	0	1	0	1	0	1	0	1	1	1	0	LCD display ON/OFF 0: OFF, 1: ON
2). Display start line set	0	1	0	0	1	Display start address					1	Sets the display RAM display start line address
3). Page address set	0	1	0	1	0	1	1	Page address				Sets the display RAM page address
4). Column address set upper bit	0	1	0	0	0	0	1	Most significant column address				Sets the most significant 4 bits of the display RAM column address.
Column address set lower bit	0	1	0	0	0	0	0	Least significant column address				Set the least significant 4 bits of the display RAM column address.
5). Status read	0	0	1	Status				0	0	0	0	Reads the status data
6). Display data write	1	1	0	Write data								Writes to the display RAM
7). Display data read	1	0	1	Read data								Reads from the display RAM
8). ADC select	0	1	0	1	0	1	0	0	0	0	0	Sets the display RAM address SEG output correspondence 0: normal, 1: reverse
9). Display normal/reverse	0	1	0	1	0	1	0	0	1	1	0	Sets the LCD display normal/ reverse 0: normal, 1: reverse
10). Display all points ON/OFF	0	1	0	1	0	1	0	0	1	0	0	Display all points 0: normal display 1: all points ON
11). LCD bias set	0	1	0	1	0	1	0	0	0	1	0	Sets the LCD driver voltage bias ratio SPLC501C.....0:1/9, 1:1/7
12). Read/modify/write	0	1	0	1	1	1	0	0	0	0	0	Column address increment At write: +1 At read: 0
13). End	0	1	0	1	1	1	0	1	1	1	0	Clear read/modify/write
14). Reset	0	1	0	1	1	1	0	0	0	1	0	Internal reset
15). Common output mode select	0	1	0	1	1	0	0	0	*	*	*	Select COM output scan direction 0: normal direction, 1: reverse direction
16). Power control set	0	1	0	0	0	1	0	1	Operating mode			Select internal power supply operating mode
17). V _s voltage regulator internal resistor ratio set	0	1	0	0	0	1	0	0	Resistor ratio			Select internal resistor ratio (Rb/Ra) mode
18). Electronic volume mode set	0	1	0	1	0	0	0	0	0	0	1	Set the V _s output voltage electronic volume register
Electronic volume register set	0	1	0	*	*	Electronic volume value						

Command	Command Code											Function
	A0P	$\overline{RD}$	$\overline{WR}$	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	
19). Static indicator ON/OFF Static indicator Register set				1	0	1	0	1	1	0	0	0: OFF, 1: ON 1 Set the flashing mode
20). Page Blink Page selection	0	1	0	1	1	0	1	0	1	0	1	P7 - 0: 1 - blinking page 0 - no blinking, normal display
21). Driving Mode Set Mode selection	0	1	0	1	1	0	1	0	0	1	0	Set the driving mode register Driving capability (D1, D0): (1,1)>(0,0)>(0,1)>(1,0)
22). Power saver												Display OFF and display all points ON compound command
23). NOP	0	1	0	1	1	1	0	0	0	1	1	Command for non-operation
24). Test	0	1	0	1	1	1	1	*	*	*	*	Command for IC test Do not use this command

5. Block Diagram



6. Mechanical Outline



Note:

1. Operating Voltage: 3.3V
2. Display Type: FSTN
3. Drive Method: 1/64Duty, 1/9 Bias
4. Viewing Direction: 6:00
5. Operating Temp: -20°C~70°C
Storage Temp: -30°C~85°C
6. Backlight: LED/BLUE
7. Resolution: 128X64 Dots

